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SERIAL NUMBER 10/690,084	FILING OR 371(c) DATE 10/20/2003 RULE	CLASS 438	GROUP ART UNIT 1765	ATTORNEY DOCKET NO. NOVLP068/NVLS-000818
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** CONTINUING DATA ***** YF LV

** FOREIGN APPLICATIONS ***** NONE LV

IF REQUIRED, FOREIGN FILING LICENSE GRANTED **
 03/12/2004

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after Allowance	STATE OR COUNTRY AZ	SHEETS DRAWING 3	TOTAL CLAIMS 47	INDEPENDENT CLAIMS 3
Verified and Acknowledged	Examiner's Signature <u>[Signature]</u> Initials <u>LV</u>				

ADDRESS

22434

TITLE

Method for fabrication of semiconductor interconnect structure with reduced capacitance, leakage current, and improved breakdown voltage

FILING FEE RECEIVED
2670

FEES: Authority has been given in Paper
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